IN THE CLAIMS:

Please amend claims 1-10, 16, 20, 21, 24-27, 29, 34 and 36 as indicated in the following.

Claims Listing:

1. (Currently Amended) A method comprising ~~the steps of~~:
identifying a first display content to be displayed at a first time;
identifying a second display content to be displayed at a second time, wherein the second
time is after the first time;
providing display data to a display port at a first frame rate, when the first display content
is different from the second display content; and
providing display data to the display port at a second frame rate, when the first display
content is substantially the same as the second display content, wherein the
second frame rate is less than the first frame rate.

2. (Currently Amended) The method as in Claim 1, further including ~~the steps of~~:
enabling a first clock rate, when the first display content is different from the second
display content; and
enabling a second clock rate, when the first display content is substantially the same as
the second display content, wherein the second clock rate is less than the first
clock rate.

3. (Currently Amended) The method as in Claim 2, wherein ~~the step of~~ enabling the first
clock rate includes:
providing a clock signal associated with an oscillator to a phase locked loop; and
providing a locked clock signal generated by the phase locked loop to a clock bus.

4. (Currently Amended) The method as in Claim 3, further wherein ~~the step of~~ enabling
the second clock rate includes:
disabling the phase locked loop; and
providing the clock signal associated with the oscillator to the clock bus.

5. (Currently Amended) The method as in Claim 1, further including the steps of:
representing the display data using a first number of bits, when the first display content is
　　　different from the second display content; and
representing the display data using a second number of bits, when the first display
　　　content is substantially the same as the second display content, wherein the
　　　second number of bits is less than the first number of bits.

6. (Currently Amended) The method as in Claim 5, wherein the first and second numbers
of bits [[is]]are associated with a color depth.

7. (Currently Amended) The method as in Claim 1, further including the steps of:
activating a first number of interface lines associated with the display port, when the first
　　　display content is different from the second display content; and
activating a second number of interface lines associated with the display port, when the
　　　first display content is substantially the same as the second display content,
　　　wherein the second number of control lines is less than the first number of control
　　　lines associated with the display port.

8. (Original) The method as in Claim 7, wherein the interface lines include one of digital
to analog converter input lines, transition minimized differential signaling input lines, or low
voltage differential signaling input lines.

9. (Currently Amended) The method as in Claim 1, further including the steps of:
identifying a third display content to be displayed at a third time, wherein the third time is
　　　after the second time;
providing display data with a first color depth, when the third display content is different
　　　from the first display content; and
providing display data with a second color depth, when the third display content is
　　　substantially the same as the first display content, when the second color depth is
　　　less than the first color depth.

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10. (Currently Amended) The method as in Claim 1, wherein the ~~steps are performed within a portable device~~display port is a display port of a portable device.

11. (Original) The method as in Claim 10, wherein the portable device includes a personal digital assistant.

12. (Original) The method as in Claim 1, wherein the display content is associated with a personal digital assistant.

13. (Original) The method as in Claim 1, wherein the display data is for output on a display device.

14. (Original) The method as in Claim 13, wherein the display device includes a screen associated with a personal digital assistant.

15. (Original) The method as in Claim 14, wherein the display device includes a liquid crystal display.

16. (Currently Amended) The method as in Claim 1, further including ~~the steps of~~:
supporting a first nominal power, when the first display content is different from the
       second display content; and
supporting a second nominal power, when the first display content is substantially the
       same as the second display content, wherein the second nominal power is less
       than the first nominal power.

17. (Original) The method as in Claim 1, wherein a number of bits used to represent multimedia data is changed to match a change in nominal power.

18. (Original) The method as in Claim 17, wherein the multimedia data includes video data.

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19. (Original) The method as in Claim 17, wherein the multimedia data includes audio data.

20. (Currently Amended) A method comprising ~~the steps of~~:
identifying a first display content to be displayed at a first time;
identifying a second display content to be displayed at a second time, wherein the second
    time is after the first time;
using a first number of bits to represent <u>color information of</u> display data, when the first
    display content is different from the second display content; and
using a second number of bits to represent <u>color information of</u> display data, when the
    first display content is substantially the same as the second display content,
    wherein the ~~second frame rate is less than the first frame rate~~<u>second number of</u>
    <u>bits is less than the first number of bits</u>.

21. (Currently Amended) The method as in Claim 20, wherein the <u>color information</u>
includes~~number of bits used to represent data includes~~ a color depth.

22. (Original) The method as in Claim 20, wherein the display data is for display on a display device.

23. (Original) The method as in Claim 22, wherein the display device includes a personal digital assistant screen.

24. (Currently Amended) The method as in Claim 20, further including ~~the steps of~~:
enabling a first clock rate, when the first display content is different [[form]]<u>from</u> the
    second display content; and
enabling a second clock rate, when the first display content is substantially the same as
    the second display content, wherein the second clock rate is less than the first
    clock rate.

25. (Currently Amended) The method as in Claim 24, wherein ~~the step of~~ enabling the
first clock rate includes:

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providing a clock signal associated with an oscillator to a phase locked loop; and
providing a locked clock signal generated by the phase locked loop to a clock bus.

26. (Currently Amended) The method as in Claim 25, further wherein the step of
enabling the second clock rate includes:
　　disabling the phase locked loop; and
　　providing the clock signal associated with the oscillator to the clock bus.

27. (Currently Amended) The method as in Claim 20, further including the steps of:
　　supporting a first nominal power, when the first display content is different from the
　　　　second display content; and
　　supporting a second nominal power, when the first display content is substantially the
　　　　same as the second display content, wherein the second nominal power is less
　　　　than the first nominal power.

28. (Original) A system comprising:
　　a content analyzer to compare a first display content to be displayed at a first time with a
　　　　second display content to be displayed at a second time, wherein the second time
　　　　is after the first time;
　　a display module to alter a frame rate for providing display data to a display port, wherein
　　　　said frame rate is based on the comparison performed by said content analyzer;
　　　　and
　　said display port to output said display data.

29. (Currently Amended) The system as in Claim 28, wherein said display module
further used further is to:
　　apply a first frame rate for providing display data to said display port, when said content
　　　　analyzer determines the first display content is different from the second display
　　　　content; and
　　apply a second frame rate for providing display data to said display port, when said
　　　　content analyzer determines the first display content is substantially the same as

the second display content, wherein the second frame rate is less than the first frame rate.

30. (Original) The system as in Claim 28, wherein said first display content is stored in memory.

31. (Original) The system as in Claim 28, further including a power module, said power module to:

    enable a first clock rate, when said content analyzer determines the first display content is different from the second display content; and

    enable a second clock rate, when said content analyzer determines the first display content is substantially the same as the second display content, wherein said second clock rate is less than said first clock rate.

32. (Original) The system as in Claim 28, further including a power module, said power module to:

    support a first nominal power, when said content analyzer determines the first display content is different from the second display content; and

    support a second nominal power when said content analyzer determines the first display content is substantially the same as the second display content, wherein said second nominal power is less than said first nominal power.

33. (Original) The system as in Claim 28, wherein:

said content analyzer further used to compare a third display content to be displayed at a third time with the first display content, wherein the third time is after the second time; and

said display module further to:

    provide display data with a first color depth, when the content analyzer determines the third display content is different from the first display content; and

provide display data with a second color depth, when the content analyzer
determines the third display content is substantially the same as the first
display content.

34. (Currently Amended) A system comprising:
a content analyzer to compare a first display content to be displayed at a first time with a
second display content to be displayed at a second time, wherein the second time
is after the first time;
a display module to alter a number of bits used to represent color information of display
data, wherein said number of bits is based on the comparison performed by said
content analyzer; and
said display port to output said display data.

35. (Original) The system as in Claim 34, wherein said display module further used to:
apply a first number of bits used to represent display data, when said content analyzer
determines said first display content is different from said second display content;
apply a second number of bits used to represent display data, when the first display
content is substantially the same as the second display content, wherein the
second number of bits used to represent display data is less than the first number
of bits used to represent display data.

36. (Currently Amended) The system as in Claim 34, wherein the ~~number of bits used to~~
~~represent data is associated with~~color information includes a color depth of the display data.

37. (Original) The system as in Claim 34, wherein said display port is coupled to one of
a digital to analog converter, a transition minimized differential signaling transceiver, or a low
voltage differential signaling transceiver, for providing display data to a display device.

38. (Original) The system as in Claim 37, wherein said display device includes a screen
of a personal digital assistant.

39. (Original) The system as in Claim 38, wherein said display device includes a liquid crystal display.

40. (Original) The system as in Claim 34, further including a power module, said power module to:

    enable a first clock rate, when said content analyzer determines the first display content is different from the second display content; and

    enable a second clock rate, when said content analyzer determines the first display content is substantially the same as the second display content, wherein said second clock rate is less than said first clock rate.

41. (Original) The system as in Claim 34, further including a power module, said power module to:

    support a first nominal power, when said content analyzer determines the first display content is different from the second display content; and

    support a second nominal power when said content analyzer determines the first display content is substantially the same as the second display content, wherein said second nominal power is less than said first nominal power.